

Amendments to the Specification

Please replace the paragraph at page 2, lines 1-11 of the specification with the following paragraph:

In the example as shown, for the closed loop control, the output voltage U_0 is applied via a voltage divider 28, 30 and a P element formed by an operational amplifier 32 to a pulse width modulator module 34. The voltage divider 28, 30 is dimensioned so that when the desired output voltage U_0 appears at the junction between the resistances 28 and 30 a voltage is generated substantially the same as the reference voltage U_{REF} at the input of the P element 32. The P element 32 thus generates a P element control voltage which is applied to the pulse width modulator module 34 for driving the switched power supply so that the desired output voltage is obtained. As illustrated in Fig. 1 the pulse width modulator module 34 is represented together with an input amplifier stage 36, it further ~~comprising~~ comprises at least one ~~storage register and a counter~~ storage register 35 and a counter 37 forming an adjustment unit (~~not shown~~) for setting the pulse width modulator.